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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>( Not for submission under 37 CFR 1.99)</i>	Application Number	10814615
	Filing Date	2004-03-31
	First Named Inventor	Andrew W. Dornbusch
	Art Unit	2611
	Examiner Name	BAYARD, Emmanuel
	Attorney Docket Number	1052-0010

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	4	6999747		2006-02-14	Su, Ting-Ming	
	5	6308058		2001-10-21	Souetinov et al.	
	6	6269127		2001-07-31	Richards, Glenn L.	
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5	"A Single-Chip 2.4-GHz Direct-Conversion CMOS Receiver for Wireless Local Loop using Multiphase Reduced Frequency Conversion Technique," Lee, Kyeongho et al, IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, pp. 800-809	<input type="checkbox"/>
6	"An Image-Rejecting Mixer and Vector Filter with 55-dB Image Rejection over Process, Temperature, and Transistor Mismatch," Hornak, Thomas et al., IEEE Journal of Solid-State Circuits, Vol. 36, No. 1, pp. 23-33	<input type="checkbox"/>

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See attached certification statement.  
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.  
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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/paul j polansky/	Date (YYYY-MM-DD)	2007-08-14
Name/Print	Paul J. Polansky	Registration Number	33,992

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